

Youtao Zhang

Associate Professor
Computer Science Department
University of Pittsburgh
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RESEARCH INTERESTS

- Compilers and program analysis: update-conscious compilation; program slicing; program profiling; code optimization.
- Computer architecture: novel memory technologies; architectural support for security; chip multiprocessors; cache management; onchip interconnection.

EDUCATION

- Ph.D., Department of Computer Science, University of Arizona, Tucson, AZ, August 2002, Thesis: “The Design and Implementation of Compression Techniques for Profile Guided Compilation”, Advisor: Professor Rajiv Gupta.
- M.Eng., Department of Computer Science and Technology, Nanjing University, China, June 1996.
- B.S., Department of Computer Science and Technology, Nanjing University, China, June 1993.

EMPLOYMENT

- Associate Professor, University of Pittsburgh, Pittsburgh, PA, September 2011 – present.
- Assistant Professor, University of Pittsburgh, Pittsburgh, PA, January 2006 – August 2011.
- Assistant Professor, University of Texas at Dallas, Richardson, TX, November 2002 – December 2005.

AWARDS

- NSF CAREER Award, January 2005.
- Distinguished Paper Award, IEEE/ACM International Conference on Software Engineering, May 2003.
- Most Original Paper Award, International Conference on Parallel Processing, October 2003.

I. RESEARCH

PUBLICATIONS

JOURNAL PUBLICATIONS

- [J-1] JSA Yang Zhao, Youtao Zhang, Zhiguang Qin, Taieb Znati, “A Co-commitment based Secure Data Collection Scheme for Tiered Wireless Sensor Networks,” *Journal of System Architecture*, Vol. 57(6), pages 655–662, 2011.
- [J-2] JPDC Jun Yang, Lan Gao, Youtao Zhang, Marek Chrobak, and Hsien-Hsin S Lee, “A Low-cost Memory Remapping Scheme for Address Bus Protection,” *Journal of Parallel and Distributed Computing*, Vol. 70(5), pages 443–457, 2010.
- [J-3] TACO Xiuyi Zhou, Jun Yang, Marek Chrobak, and Youtao Zhang, “Performance-aware thermal management via task scheduling,” *ACM Transactions on Architecture and Code Optimization*, Vol. 7(1), pages 5:1–5:31, 2010.
- [J-4] WINET Youtao Zhang, Jun Yang, Hai T. Vu, and Yizhi Wu, “The Design and Evaluation of Interleaved Authentication for Filtering False Reports in Multipath Routing WSNs,” *Wireless Networks*, Springer, Vol. 16(1), pages 125–140, 2010.

- [J-5] TPDS Xiuyi Zhou, Jun Yang, Yi Xu, Youtao Zhang, Jianhua Zhao, “Thermal-Aware Task Scheduling for 3D Multicore Processors,” *IEEE Transactions on Parallel and Distributed Systems*, Vol. 21(1), pages 60–71, 2010.
- [J-6] IEEE Micro Benjamin C. Lee, Ping Zhou, Doug Burger, Engin Ipek, Onur Mutlu, Jun Yang, Youtao Zhang, and Bo Zhao, “Phase Change Technology and the Future of Main Memory (tentative title),” *IEEE Micro, Special Issue: Micro’s Top Picks from 2009 Computer Architecture Conferences (MICRO TOP PICKS)*, Vol. 30(1), January/February 2010.
- [J-7] JNCA Youtao Zhang, Jun Yang, Weijia Li, Linzhang Wang, and Lingling Jin, “An Authentication Scheme for Locating Compromised Sensor Nodes in WSNs,” *Journal of Network and Computer Applications*, Elsevier, Vol 33(1), pages 50–62, 2010.
- [J-8] TACO Weijia Li, Youtao Zhang, Jun Yang, and Jiang Zheng, “Towards Update-Conscious Compilation for Energy-Efficient Code Dissemination in WSNs,” *ACM Transactions on Architecture and Code Optimization*, Vol. 6(4), 2009.
- [J-9] JCEE Youtao Zhang, Jun Yang, and Lan Gao, “Supporting Flexible Streaming Media Protection through Privacy-aware Secure Processors,” a special issue on Circuits and Systems for Real-Time Security and Copyright Protection of Multimedia, *Journal of Computers and Electrical Engineering*, Elsevier, Vol. 35(2), pages 286–299, 2009.
- [J-10] JSS Yongjing Lin, Youtao Zhang, and Rajiv Gupta, “The Design and Evaluation of Path Matching Schemes on Compressed Control Flow Traces”, *Journal of Systems and Software*, Elsevier, Vol. 80(3), pages 396–409, 2007.
- [J-11] SP&E Youtao Zhang and Rajiv Gupta, “Compressing Heap Data for Improved Memory Performance,” *Software Practice & Experience*, Vol. 36(10), pages 1081–1111, 2006.
- [J-12] TC Jun Yang, Lan Gao, and Youtao Zhang, “Improving Memory Encryption Performance in Secure Processors,” *IEEE Transactions on Computers*, Vol. 54(5), pages 630–640, 2005. **Featured in MIT Technology Review, July 2005.**
- [J-13] JSA Jun Yang, Jia Yu, and Youtao Zhang, “A Low Energy Cache Design for Multimedia Applications Exploiting Set Access Locality,” *Journal of Systems Architecture*, Elsevier, Vol. 51(10–11), pages 653–664, 2005.
- [J-14] TOPLAS Xiangyu Zhang, Rajiv Gupta, and Youtao Zhang, “Cost and Precision Tradeoffs of Dynamic Slicing Algorithms,” *ACM Transactions on Programming Languages and Systems*, Vol. 27(4), pages 631–661, 2005.
- [J-15] JEC Youtao Zhang and Jun Yang, “Reducing I-cache Energy of Multimedia Applications through LowCost Tag Comparison Elimination,” *Journal of Embedded Computing*, IOS Press, Vol. 1(4), pages 461–470, 2005.
- [J-16] IHPCN Zili Shao, Qingfeng Zhuge, Youtao Zhang, Edwin H.-M. Sha, “Algorithms and Analysis of Scheduling for Low-Power High-Performance DSP on VLIW Processors,” *International Journal of High Performance Computing and Networking*, Vol. 1, pages 3–16, 2004.

CONFERENCE PUBLICATIONS

- [C-1] DAC Lei Jiang, Bo Zhao, Youtao Zhang, and Jun Yang, “Constructing Large and Fast Multi-level Cell STT-MRAM based Cache for Embedded Processors,” *ACM/IEEE the 49th Design Automation Conference*, San Francisco, CA, June 2012.
- [C-2] HPCA Lei Jiang, Bo Zhao, Youtao Zhang, Jun Yang, and Bruce R. Childers, “Improving Write Operations in MLC Phase Change Memory,” *IEEE the 18th International Symposium on High-Performance Computer Architecture*, New Orleans, Louisiana, February 2012.
- [C-3] ISLPED Lei Jiang, Youtao Zhang, and Jun Yang, “Enhancing Phase Change Memory Lifetime through Fine-Grained Current Regulation and Voltage Upscaling,” *The International Symposium on Low Power Electronics and Design*, Fukuoka, Japan, August 2011. (acceptance rate = n/a).
- [C-4] ICS Yi Xu, Yu Du, Youtao Zhang, and Jun Yang, “A Composite and Scalable Cache Coherence Protocol for Large Scale CMPs,” *The 25th International Conference on Supercomputing*, pages 285–294, Tucson, Arizona, June 2011. (acceptance rate = 35/161 = 21.7%).

- [C-5] DSN Lei Jiang, Yu Du, Youtao Zhang, Bruce R. Childers, and Jun Yang, "LLS: Cooperative Integration of Wear-Leveling and Salvaging for PCM Main Memory," *The 41st Annual IEEE/IFIP International Conference on Dependable Systems and Networks*, Hong Kong, China, June 2011. (DCCS track acceptance rate = 26/148=17.6%).
- [C-6] ISPASS Santiago Bock, Bruce R. Childers, Rami Melhem, Daniel Mosse, and Youtao Zhang, "Analyzing the Impact of Useless Write-backs on Endurance and Energy Consumption of PCM Main Memory," *IEEE International Symposium on Performance Analysis of Systems and Software*, pages 56–65, Austin, Texas, April 2011. (acceptance rate = n/a).
- [C-7] DATE Lin Li, Youtao Zhang, and Jun Yang, "Proactive Recovery for BTI in High-k SRAM Cells," *The IEEE conference on Design, Automation and Test in Europe*, pages 1–6, Grenoble, France, 2011. (acceptance rate = 211/781=27%).
- [C-8] IPDPS Ping Zhou, Yu Du, Youtao Zhang, and Jun Yang, "Fine-Grained QoS Scheduling for PCM-based Main Memory Systems," *IEEE the 24th International Parallel & Distributed Processing Symposium*, Atlanta, Georgia, April 2010. (acceptance rate = 127/527 = 24%).
- [C-9] LCTES Weijia Li, and Youtao Zhang, "An Efficient Code Update Scheme for DSP Applications in Mobile Embedded Systems," *ACM SIGPLAN/SIGBED Conference on Languages, Compilers and Tools for Embedded Systems*, Stockholm, Sweden, April 2010. (acceptance rate = 18/58 = 31%).
- [C-10] DATE Lin Li, Youtao Zhang, Jun Yang, and Jianhua Zhao, "Proactive NBTI Mitigation for Busy Functional Units in Out-of-Order Microprocessors," *The IEEE conference on Design, Automation and Test in Europe*, Dresden, Germany, March 8-12, 2010. (acceptance rate = 26%).
- [C-11] HPCA Yi Xu, Bo Zhao, Youtao Zhang, and Jun Yang, "Simple Virtual Channel Allocation for High Throughput and High Frequency On-Chip Routers," *IEEE the 16th International Symposium on High-Performance Computer Architecture*, Bangalore, India, January 2010. (acceptance rate = 32/175 = 18%).
- [C-12] MICRO Bo Zhao, Yu Du, Youtao Zhang, and Jun Yang, "Variation-Tolerant Non-Uniform 3D Cache Management in Die Stacked Multicore Processor," *The 42nd Annual IEEE/ACM International Symposium on Microarchitecture*, New York, December 2009. (acceptance rate = 52/210 = 25%).
- [C-13] ICCAD Ping Zhou, Bo Zhao, Jun Yang, and Youtao Zhang, "Energy Reduction for STT-RAM Using Early Write Termination," *IEEE/ACM International Conference on Computer-Aided Design*, San Jose, CA, November 2009. acceptance rate = 115/438 = 26%).
- [C-14] ISCA Ping Zhou, Bo Zhao, Jun Yang, and Youtao Zhang, "A Durable and Energy Efficient Main Memory Using Phase Change Memory Technology," *The 36th International Symposium on Computer Architecture*, Austin, Texas, June 2009. (acceptance rate = 43/210 = 20%).
- [C-15] RTCSA Yang Zhao, Youtao Zhang, Zhiguang Qin, and Taieb Znati, "SDC: Secure Data Collection for Time Based Queries in Tiered Wireless Sensor Networks," *The 15th IEEE International Conference on Embedded and Real-Time Computing Systems and Applications*, Beijing, China, August 2009. (acceptance rate: 39/124=31%).
- [C-16] DCOSS Weijia Li, Youtao Zhang, and Bruce Childers, "MCP: an Energy-Efficient Code Distribution Protocol for Multi-Application WSNs," *The 5th IEEE International Conference on Distributed Computing in Sensor Systems*, LNCS 5516, Springer-Verlag, pages 259-272, Marina Del Rey, California, June 2009. (acceptance rate = 26/116 = 22%).
- [C-17] HPCA Yi Xu, Yu Du, Bo Zhao, Xiuyi Zhou, Youtao Zhang, and Jun Yang, "A Low-Radix and Low-Diameter 3D Interconnection Network Design," *IEEE the 15th International Symposium on High-Performance Computer Architecture*, pages 30–41, Raleigh, North Carolina, 2009. **Nominated for best paper award.** (acceptance rate = 35/184 = 19%).
- [C-18] ASPDAC Ping Zhou, Bo Zhao, Yi Xu, Yu Du, Youtao Zhang, Jun Yang, Li Zhao, "Frequent Value Compression in Packet-based NoC Architecture," *The 14th Asia and South Pacific Design Automation Conference*, Yokohama, Japan, January 2009. (acceptance rate = 116/355 = 33%).

- [C-19] EUC Weijia Li, Yu Du, Youtao Zhang, Bruce Childers, Ping Zhou, Jun Yang, "Adaptive Buffer Management for Efficient Code Dissemination in Multi-Application Wireless Sensor Networks," *International Conference on Embedded and Ubiquitous Computing*, Shanghai, China, 2008. (acceptance rate = $70/233 = 30\%$).
- [C-20] ICPP Xiuyi Zhou, Yi Xu, Yu Du, Youtao Zhang and Jun Yang, "Thermal Management for 3D Processors via Task Scheduling," *International Conference on Parallel Processing*, Portland, Oregon, September 2008. (acceptance rate = $81/263 = 30\%$).
- [C-21] ISPASS Jun Yang, Xiuyi Zhou, Marek Chrobak, Youtao Zhang, Lingling Jin, "Dynamic Thermal Management through Task Scheduling," *International Symposium on Performance Analysis of Systems and Software*, April 2008. (acceptance rate = $22/63 = 35\%$).
- [C-22] PLDI Weijia Li, Youtao Zhang, Jun Yang, and Jiang Zheng, "UCC: Update-conscious Compilation for Energy Efficiency in Wireless Sensor Networks," *ACM SIGPLAN Conference on Programming Language Design and Implementation*, San Diego, California, 2007. (acceptance rate = $45/178 = 25\%$).
- [C-23] MASS Weijia Li, Youtao Zhang, and Jun Yang, "Dynamic Authentication-Key Re-assignment for Reliable Report Delivery," *The 3rd IEEE International Conference on Mobile Ad-hoc and Sensor Systems*, Vancouver, Canada, October 2006. (acceptance rate = $49/197 = 25\%$).
- [C-24] ICCD Lingling Jin, Wei Wu, Jun Yang, Chuanjun Zhang, and Youtao Zhang, "Reduce Register Files Leakage Through Discharging Cells," *International Conference on Computer Design*, California, October 2006. (acceptance rate = $45/178 = 25\%$).
- [C-25] PACT Lan Gao, Jun Yang, Marek Chrobak, Youtao Zhang, San Nguyen, Hsien-Hsin Lee, "A Low-cost Memory Remapping Scheme for Address Bus Protection," *The 15th International Conference on Parallel Architectures and Compilation Techniques*, Seattle, Washington, September 2006. (acceptance rate = $30/117 = 26\%$).
- [C-26] DCOSS Youtao Zhang, Jun Yang, Lingling Jin, and Weijia Li, "Locating Compromised Sensor Nodes through Incremental Hashing Authentication," *IEEE International Conference on Distributed Computing in Sensor Systems*, San Francisco, June 2006. (acceptance rate = $33/87 = 38\%$).
- [C-27] IPDPS Youtao Zhang, Jun Yang, and Hai T Vu, "The Interleaved Authentication for Filtering False Reports in Multipath Routing based Sensor Networks," *IEEE 20th International Parallel and Distributed Processing Symposium*, Rhodes Island, Greece, April 2006. (acceptance rate = $125/531 = 24\%$).
- [C-28] HPCA Weidong Shi, Joshua B. Fryman, Guofei Gu, Hsien-Hsin S. Lee, Youtao Zhang, and Jun Yang, "InfoShield: A Security Architecture for Protecting Information Usage in Memory," *IEEE 12th International Symposium on High-Performance Computer Architecture*, Austin, TX, February 2006. (acceptance rate = $26/172 = 15\%$).
- [C-29] HPCA Youtao Zhang, Lan Gao, Jun Yang, Xiangyu Zhang, and Rajiv Gupta, "SENS: Security Enhancement to Symmetric Shared Memory Multiprocessors," *IEEE 11th International Symposium on High Performance Computer Architecture*, San Francisco, California, February 2005. (acceptance rate = $28/181 = 15\%$).
- [C-30] SAC Yongjing Lin, Youtao Zhang, Quanzhong Li, and Jun Yang, "Supporting Efficient Query Processing on Compressed XML Files," *ACM The 20th Annual Symposium on Applied Computing*, Santa Fe, New Mexico, March, 2005. (acceptance rate = $278/764 = 36\%$).
- [C-31] DCC Yongjing Lin, and Youtao Zhang, "Performance Comparison of Path Matching Algorithms over Compressed Control Flow Traces," *IEEE Data Compression Conference*, Snowbird, Utah, March 2005. (acceptance rate = n/a).
- [C-32] ICSS Lingling Jin, Wei Wu, Jun Yang, Chuanjun Zhang, and Youtao Zhang, "Dynamic Co-allocation of Level One Caches," *The Second International Conference on Embedded Software and Systems*, pages 373-385, Xi'an, China 2005. (acceptance rate = $68/361 = 19\%$).
- [C-33] ICSE Xiangyu Zhang, Rajiv Gupta, and Youtao Zhang, "Efficient Forward Computation of Dynamic Slices Using Reduced Ordered Binary Decision Diagrams," *IEEE/ACM International Conference on Software Engineering*, Edinburgh, UK, May 2004. (acceptance rate = $58/436 = 13\%$).

- [C-34] ICPADS Guodong Li, Youtao Zhang, Yongjing Lin, Yaochun Huang, "Scalable Duplication Strategy with Bounded Availability of Processors," *IEEE The Tenth International Conference on Parallel and Distributed Systems*, Newport Beach, California, July 2004. (acceptance rate = 65/292 = 22%).
- [C-35] MICRO Jun Yang, Youtao Zhang and Lan Gao, "Fast Secure Processor for Inhibiting Software Piracy and Tampering," *IEEE/ACM 36th International Symposium on Microarchitecture*, pages 351-360, San Diego, December 2003. (acceptance rate = 35/134 = 26%).
- [C-36] ICPP Youtao Zhang and Rajiv Gupta, "Enabling Partial Cache Line Prefetching Through Data Compression," *International Conference on Parallel Processing*, pages 277-285, Kaohsiung, Taiwan, October 2003. (acceptance rate = 69/192 = 36%). **ICPP 2003 Most Original Paper Award.**
- [C-37] ICPP Youtao Zhang and Jun Yang, "Procedural Level Address Offset Assignment of DSP Applications with Loops," *International Conference on Parallel Processing*, pages 21-28, Kaohsiung, Taiwan, October 2003. (acceptance rate = 69/192 = 36%).
- [C-38] ICSE Xiangyu Zhang, Rajiv Gupta, and Youtao Zhang, "Precise Dynamic Slicing Algorithms," *IEEE/ACM International Conference on Software Engineering*, pages 319-329, Portland, Oregon, May 2003. (acceptance rate = 42/324 = 13%). **ICSE 2003 Distinguished Paper Award.**
- [C-39] ISLPED Youtao Zhang and Jun Yang, "Low Cost Instruction Cache Designs for Tag Comparison Elimination," *ACM/IEEE International Symposium on Low Power Electronics and Design*, pages 266-269, Seoul, Korea, August 2003. (acceptance rate = 90/221 = 41%).
- [C-40] ISLPED Jun Yang, Jia Yu, and Youtao Zhang, "Lightweight Set Buffer: Low Power Data Cache for Multimedia Applications," *ACM/IEEE International Symposium on Low Power Electronics and Design*, pages 270-273, Seoul, Korea, August 2003. (acceptance rate = 90/221 = 41%).
- [C-41] CC Youtao Zhang and Rajiv Gupta, "Data Compression Transformations for Dynamically Allocated Data Structures," *International Conference on Compiler Construction*, LNCS 2304, Springer Verlag, pages 14-28, Grenoble, France, April 2002. (acceptance rate = 18/44 = 41%).
- [C-42] CC Rajiv Gupta, Eduard Mehofer, and Youtao Zhang, "A Representation for Bit Section based Analysis and Optimization," *International Conference on Compiler Construction*, LNCS 2304, Springer Verlag, pages 62-77, France, April 2002. (acceptance rate = 18/44 = 41%).
- [C-43] DCC Youtao Zhang and Rajiv Gupta, "Path Matching in Compressed Control Flow Traces," *IEEE Data Compression Conference*, pages 132-141, Snowbird, Utah, April 2002. (acceptance rate = n/a).
- [C-44] PLDI Youtao Zhang and Rajiv Gupta, "Timestamped Whole Program Path Representation and its Applications," *ACM SIGPLAN Conference on Programming Language Design and Implementation*, pages 180-190, Snowbird, Utah, June 2001. (acceptance rate = 30/144 = 21%).
- [C-45] MICRO Jun Yang, Youtao Zhang, and Rajiv Gupta, "Frequent Value Compression in Data Caches," *IEEE/ACM 33rd International Symposium on Microarchitecture*, pages 258-265, Monterey, CA, December 2000. (acceptance rate = 31/110 = 28%).
- [C-46] ASPLOS Youtao Zhang, Jun Yang, and Rajiv Gupta, "Frequent Value Locality and Value-Centric Data Cache Design," *ACM 9th International Conference on Architectural Support for Programming Languages and Operating Systems*, pages 150-159, Cambridge, MA, November 2000. (acceptance rate = 24/114 = 21%).

BOOK CHAPTERS

- [B-1] JW&S Youtao Zhang, Rajiv Gupta, "Enabling Partial Cache Line Prefetching Through Data Compression," *High-Performance Computing: Paradigm and Infrastructure*, pages 183-200, John Wiley & Sons, Inc., October 2005.
- [B-2] CRC Rajiv Gupta, Eduard Mehofer and Youtao Zhang, "Profile Guided Code Optimizations," *The Compiler Design Handbook: Optimizations and Machine Code Generation*, Chapter 4, CRC Press, September 2002.

WORKSHOP/POSTER PUBLICATIONS

- [IP-1] DATE Bo Zhao, Jun Yang, Youtao Zhang, Yiran Chen, and Hai Li, "Architecting a Common-Source-Line Array for Bipolar Non-Volatile Memory Devices," (interactive presentation), *The IEEE conference on Design, Automation and Test in Europe*, Dresden, Germany, March 2012.
- [P-2] PACT Ping Zhou, Bo Zhao, Youtao Zhang, Jun Yang, and Yiran Chen, "MRAC: A Memristor-based Reconfigurable Framework for Adaptive Cache Replacement," (poster), *The 20th International Conference on Parallel Architectures and Compilation Techniques*, Galveston Island, Texas, October 2011.
- [W-3] NSFNGS Youtao Zhang, Jun Yang and Weijia Li, "Towards Energy-Efficient Code Dissemination in Wireless Sensor Networks," *The Next Generation Software Workshop*, collocated with IPDPS, Florida, April 2008.
- [W-4] ASAP Youtao Zhang, Jun Yang, and Lan Gao, "Efficient Group Key Management with Tamper-resistant ISA Extensions," *IEEE 17th International Conference on Application-specific Systems, Architectures and Processors*, (Poster), Steamboat Springs, Colorado, September 2006.
- [W-5] WASSA Youtao Zhang, Jun Yang, Yongjing Lin, and Lan Gao, "Architectural Support for Protecting User Privacy on Trusted Processors," *The Workshop on Architectural Support for Security and Anti-Virus*, In conjunction with the 11th ASPLOS, Boston, MA, October 2004.
- [W-6] LCPC Bengu Li, Youtao Zhang and Rajiv Gupta, "Speculative Subword Register Allocation in Embedded Processors," *The 17th International Workshop on Languages and Compilers for Parallel Computing*, West Lafayette, Indiana, September 2004.

Invited Presentations

- "Are We Ready for PCM?",
– ESWeek 2011, ISSS+CODES Special Session, 2011.
- "The Emerging Secure Processor Designs",
– the Sixth Annual Emerging Information Technology Conference (EITC), August 2006.
- "Interleaved Authentication for Filtering False Reports in Multipath Routing based Sensor Networks",
– School of Information Sciences, University of Pittsburgh, 2006, Pittsburgh.
- "Enhancing Computer Security through Architectural Designs",
– Department of Computer Science and Technology, Nanjing University, 2005, China.
- "Enhancing Computer Security through Architectural and Compilation Designs",
– Computer Science Department, University of Pittsburgh, 2005, Pittsburgh.
- "The Design and Implementation of Compression Techniques for Profile Guided Compilation",
– CS Department, University of Texas at Dallas, 2002,
– CS Department, University of California at Davis, 2002,
– CSE Department, University of Connecticut, 2002,
– CS Department, State University of New York, Binghamton, 2002.

GRANTS

- [1] NSF, co-PI, (PI: Prof. Bruce Childers, other co-PIs: Prof. Sangyeun Cho, Prof. Daniel Mossé, Prof. Rami Melhem, Prof. Jun Yang, University of Pittsburgh), \$1,900,000, "CSR: Large: Storage Class Memory Architecture for Energy Efficient Data Centers," 6/1/2010-5/30/2014.
- [2] CRDF, University of Pittsburgh, PI, \$16,000, "Scalable Memory System Designs for Next Generation Chip-Multiprocessors," 7/1/2010-6/30/2012.
- [3] Google, Android Education, Donation of 5 Verizon DROID phones, "Experimenting Effective Code Update Techniques for Android", 2010.
- [4] NSF, co-PI, (PI: Prof. Jun Yang, ECE Department, University of Pittsburgh), \$120,000, "CSR-CSI: An Update-conscious Compilation Framework for Energy-Efficient Code Dissemination in Wireless Sensor Networks", 09/2007-09/2009.

- [5] NSF CAREER Award, PI, \$400,000, “A Compilation Framework for the Development of High Performance Secure Applications on Trusted Processors”, 01/2005-12/2009.
- [6] Emmitt Project, PI, \$45,000, University of Texas at Dallas, “Architectural support for enhancing security”, 6/2005-8/2006.
- [7] NSF, PI, \$26,000, “Collaborative: Architectural Support for Security and Privacy Protection on Uni- and Multi-Processors”, (co-PI: Prof. Jun Yang, CSE Department, University of California at Riverside), 11/2004-10/2005.

MEMBERSHIP

- ACM, IEEE, IEEE computer society.

II. TEACHING

TEACHING EXPERIENCE

University of Pittsburgh

- CS2210 Compiler Construction (graduate level), Spring 2006, 2007, 2008, 2009, 2010
- CS3220 Compiler Techniques for Parallel Systems, Fall 2007
- CS1621 Structure of Programming Languages, Fall 2007, 2009
- CS1622 Introduction to Compiler Design, Fall 2008, 2010
- CS0441 Discrete Mathematics, Spring 2009
- CS0447 Computer Organization and Assembly Language Programming, Fall 2006

University of Texas at Dallas

- CS6353 Compiler Construction (graduate level), Spring 2003, 2004, 2005, Fall 2005
- CS4348 Operating System Concepts (undergraduate level), Spring 2003, 2005
- CS5348 Operating System Concepts (graduate level), Fall 2003, 2004, Spring 2005

STUDENTS

As the degree committee chair/co-chair

- Ping Zhou, PhD student, co-advised with Prof. Jun Yang, CoE program, University of Pittsburgh
- Lei Jiang, PhD student, co-advised with Prof. Jun Yang, ECE Department, University of Pittsburgh
- Weijia Li, PhD in 2011, University of Pittsburgh. First job: Conviva Inc.
- Yang Hu, MS in 2011, University of Pittsburgh. First job: Amazon.com Inc.
- Mohammed Mohammed, MS in 2009, University of Pittsburgh.
- Hai T Vu, MS in 2005, University of Texas at Dallas.

On the PhD degree committee

- Jonathan Misurda, 2011, advisor: Prof. Bruce R. Childers, University of Pittsburgh.
- Tae Cheol Oh, 2010, advisor: Prof. Sangyeun Cho, University of Pittsburgh.
- Shuyi Shao, 2010, advisor: Prof. Rami Melhem, University of Pittsburgh.
- Hui Ling, 2010, advisor: Prof. Taieb Znati, University of Pittsburgh.

III. SERVICE

PROFESSIONAL ACTIVITIES

Guest Editor

- Journal of Embedded Computing (JEC), Special issue on Embedded System Optimization, Volume 3, Issue 1, 2009.

General Chair

- Co-chair, the 28th IEEE International Performance Computing and Communication Conference (IPCCC), December 2009.

Program Chair

- The 27th IEEE International Performance Computing and Communication Conference (IPCCC), December 2008.
- The 13th Workshop on Interaction between Compilers and Computer Architectures (Interact-13), February 2009.

Publication Chair

- The International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS), 2010, 2011.

Local Arrangement Chair

- The International Conference on Parallel Processing (ICPP), 2012.

TPC Membership

- IEEE/IFIP the 42nd Annual International Conference on Dependable Systems and Networks (DSN), 2012.
- IEEE International Parallel & Distributed Processing Symposium (IPDPS), 2011.
- The 29th IEEE International Performance Computing and Communication Conference (IPCCC), 2010, 2011.
- The Second IEEE International Symposium on Advanced Topics on Embedded Systems and Applications (ESA), 2011.
- The 9th International Symposium on Embedded Computing (EmbeddedCom), 2011.
- ACM SIGPLAN Conference on Languages, Compilers and Tools for Embedded Systems (LCTES), 2004, 2010.
- IEEE International Conference on Information Reuse and Integration (IRI), 2006, 2007, 2008, 2009, 2010, 2011.
- The International Conference on Embedded Software and System (ICCESS), 2004, 2005, 2009, 2010.
- IASTED International Conference on Parallel and Distributed Computing and Networks (PDCN), 2005, 2006, 2007, 2008, 2009, 2010.
- International Symposium on Data, Privacy, and E-Commerce (ISDPE), 2007, 2010.
- International Conference on Embedded and Multimedia Computing (EM-COM), 2009.
- IEEE the International Symposium on Embedded Computing (SEC), 2007, 2008.
- IEEE International Conference on Computer Communications and Networks (ICCCN), 2007.
- IFIP International Conference on Embedded And Ubiquitous Computing (EUC), 2005, 2006.
- ACM/IEEE International Symposium on Code Generation and Optimization (CGO), 2005.

Panelist and Reviewer

- NSF Panelist: SHF (March 2009), CRI (November 2008), CPA (March 2008), SoD (May 2006), CyberTrust (May 2005), ITR (May 2003).
- Reviewer for Estonian Science Foundation, 2010.
- Reviewer for Indiana State the 21st Century S&T Fund, 2006.

- Reviewer for many journals and conferences.

DEPARTMENT SERVICE

University of Pittsburgh

- Graduate Programs and Examinations Committee, Fall 2008 – present.
- Graduate Assessment Committee, Fall 2009 – present.
- School of Information Science, Faculty Search Committee, Spring 2010.
- Graduate admission and financial aid committee, Fall 2007 – Spring 2008.
- Undergraduate advising committee, Fall 2006 – Spring 2007.

University of Texas at Dallas

- Department Equipment Committee, Fall 2003 – Fall 2005.
- Computer Engineering Graduate Admission Committee, Spring 2004 – Fall 2005.