

CS2410 LECTURE SCHEDULE – FALL 2009

(As of October 30, 2009)

#	Date	Topic	Note
1	Sep. 2	Course introduction; technology trend	
2	Sep. 9	Technology trend, cont'd	
-	Sep. 11	ADD/DROP DEADLINE	
3	Sep. 18*	Parallel computing, motivation (Chapter 1 & 2)	* Make-up at 10am
4	Sep. 21	Shared memory programming (Chapter 3)	
5	Sep. 23	Shared memory programming (Chapter 3)	
6	Sep. 28	Shared memory programming (Chapter 4)	
7	Sep. 30	Shared memory programming (Chapter 5)	
8	Oct. 5	Reserved	
9	Oct. 7	Core design issues (H & P, Chapter 2)	
10	Oct. 13*	Core design issues (H & P, Chapter 2)	* We meet on Tuesday
11	Oct. 14	Core design issues (H & P, Chapter 2)	
12	Oct. 19	MID-TERM EXAM	
13	Oct. 21	Memory hierarchy design (Chapter 6)	
14	Oct. 26	Memory hierarchy design (Chapter 6)	
15	Oct. 28	Memory hierarchy design (Chapter 6)	
16	Nov. 2	Memory hierarchy design (Chapter 6) Multicore architecture (Chapter 13)	Paper presentation: Andrew, Josh/Adam
17	Nov. 4	Multicore memory hierarchy design (Chapter 14)	Paper presentation: Marian/Brian, Juyoung/Miao
18	Nov. 9	Multicore memory hierarchy design (Chapter 14)	Paper presentation: David/John, Pat/Dave
19	Nov. 11	Performance volatility (Chapter 15)	Paper presentation: Haifeng
20	Nov. 16	Bandwidth Wall Bottleneck (Chapter 16)	Project progress report due 11/13
21	Nov. 18	Reserved	
22	Nov. 23	Storage systems (H & P, Chapter 6)	
-	Nov. 25	THANKSGIVING BREAK – NO CLASS	
23	Nov. 30	Cache coherence problem (Chapter 8 & 11)	
24	Dec. 2	Memory consistency models (Chapter 10)	
25	Dec. 7	Project presentations	Andrew, Josh/Adam, Haifeng
26	Dec. 9	Project presentations	Marian/Brian, Juyoung/Miao
Final	Dec. 14	Project presentations	David/John, Pat/Dave
Prelim	Dec. 16*	PRELIM EXAM (1:00pm~3:00pm*) * tentative date/time	
	Dec. 18	PROJECT FINAL REPORT DUE	