

Computer Architecture Prelim Exam Reading List

November 19, 2007

Book:

[1] J. L. Hennessy and D. A. Patterson, *Computer Architecture: A Quantitative Approach*, 4th Ed., Morgan Kaufmann, 2007.

Students must at least read the following chapters to fully acquaint themselves with the book content.

Chapter 1
Chapter 2
Chapter 3
Chapter 4
Chapter 5
Chapter 6.1~6.4
Appendix A
Appendix B
Appendix C

Papers:

- [1] N. P. Jouppi. "Improving Direct-Mapped Cache Performance by the Addition of a Small Fully-Associative Cache and Prefetch Buffers," *Proc. Int'l Symp. Computer Architecture (ISCA)*, May 1990.
- [2] S. McFarling. "Combining Branch Predictors," *WRL Technical Note TN-36*, June 1993. Available at <http://www.hpl.hp.com/techreports/Compaq-DEC/WRL-TN-36.pdf>.
- [3] M. Zhang and K. Asanović. "Victim Replication: Maximizing Capacity while Hiding Wire Delay in Tiled Chip Multiprocessors," *Proc. Int'l Symp. Computer Architecture (ISCA)*, June 2005.

Refer to the prelim. exam. information at the departmental web page for other details. Topics of emphasis remain the same.